	Туре	L#	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	62621	inverter same switch\$3	00.71.	2007/05/24 13:39
2	BRS	L2	8036	microcomputer same timer	100.71	2007/05/24 13:40
3	BRS	L3	4877	microcontroller same timer	00.711	2007/05/24 13:41
4	BRS	L4	12669	2 or 3		2007/05/24 13:41
5	BRS	L5	882	1 and 4		2007/05/24 13:41
6	BRS	L6	74187	duty adj (cycle ratio)	,00.71.	2007/05/24 13:42
7	BRS	L7	213	5 and 6		2007/05/24 13:42
8	BRS	L8	99	mppt	00	2007/05/24 13:42
9	BRS	L9	122	maximum adj power adj point adj tracking	00	2007/05/24 13:43
10	BRS	L10	145	8 or 9	••••	2007/05/24 13:43
11	BRS	L11	1	7 and 10		2007/05/24 13:51
12	BRS	L12	474062	interrupt\$3	US-PGPUB; USPAT	2007/05/24 13:52
13	BRS	L13	162	7 and 12	00.717	2007/05/24 13:53
14	BRS	L14	49253	set\$4 near5 timer	100	2007/05/24 13:54
15	BRS	L15	299156	calculat\$3 near5 value		2007/05/24 13:55
16	BRS	L16	77	13 and 14		2007/05/24 13:55
17	BRS	L17	28	15 and 16	00.7.7	2007/05/24 14:20
18	BRS	L18	310	2.clm.	,	2007/05/24 14:20
19	BRS	L19	82423	12.clm.	00	2007/05/24 14:20
20	BRS	L20	5604	14.clm.		2007/05/24 14:21
21	BRS	L21	56337	15.clm.	00.711	2007/05/24 14:22
22	BRS	L22	145469	(control adj signal).clm.	00.711	2007/05/24 14:23
23	BRS	L23	14278	(set adj value).clm.	US-PGPUB; USPAT	2007/05/24 14:23

	Туре	L#	Hits	Search Text	DBs	Time Stamp
24	BRS	L24	1	18 and 19 and 20 and 21 and 22 and 23	US-PGPUB; USPAT	2007/05/24 14:24